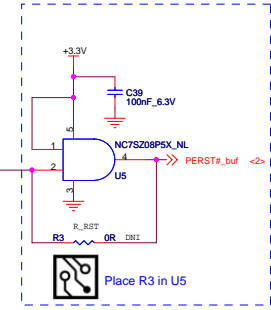
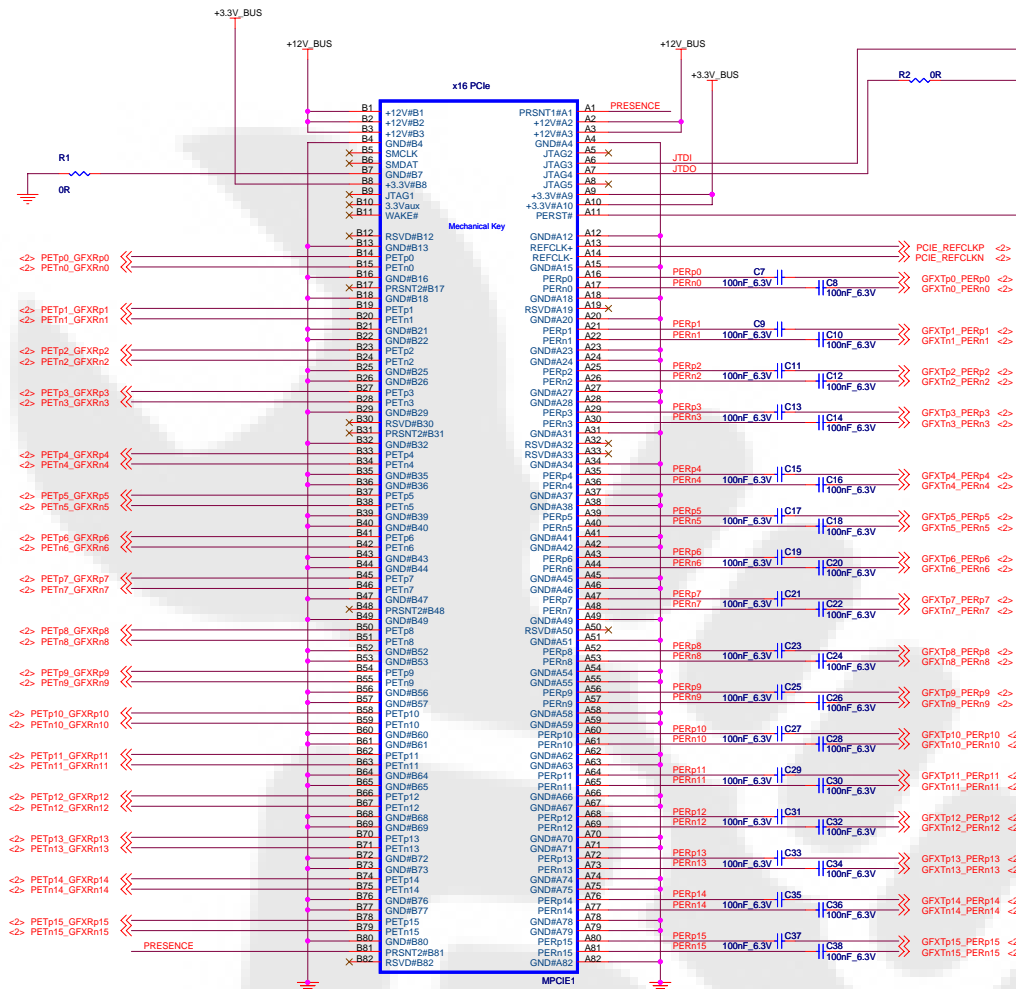
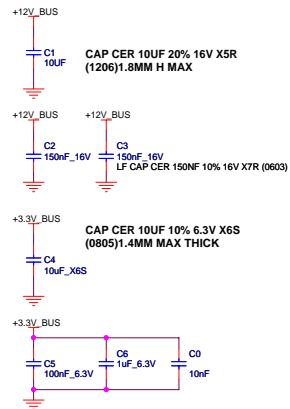
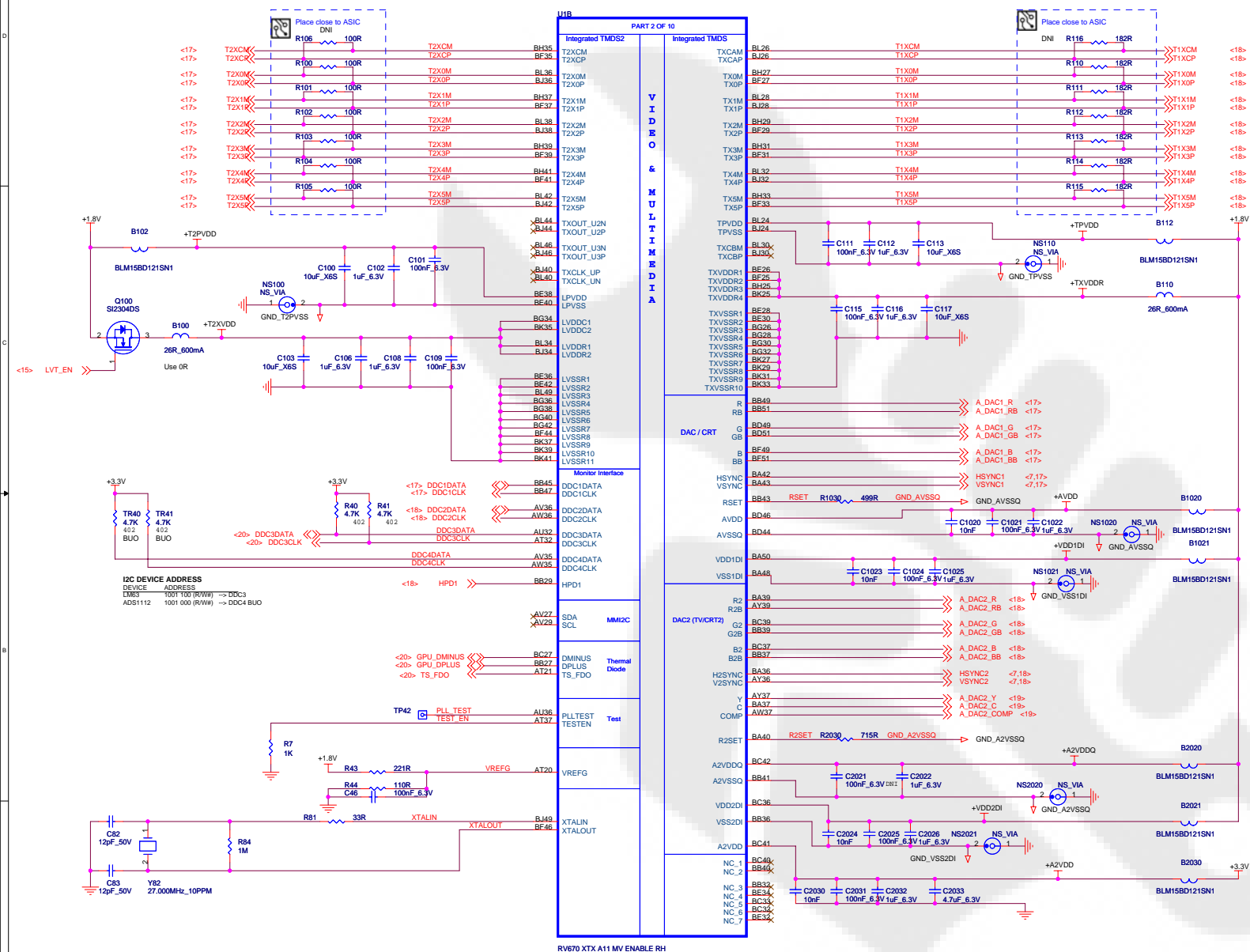


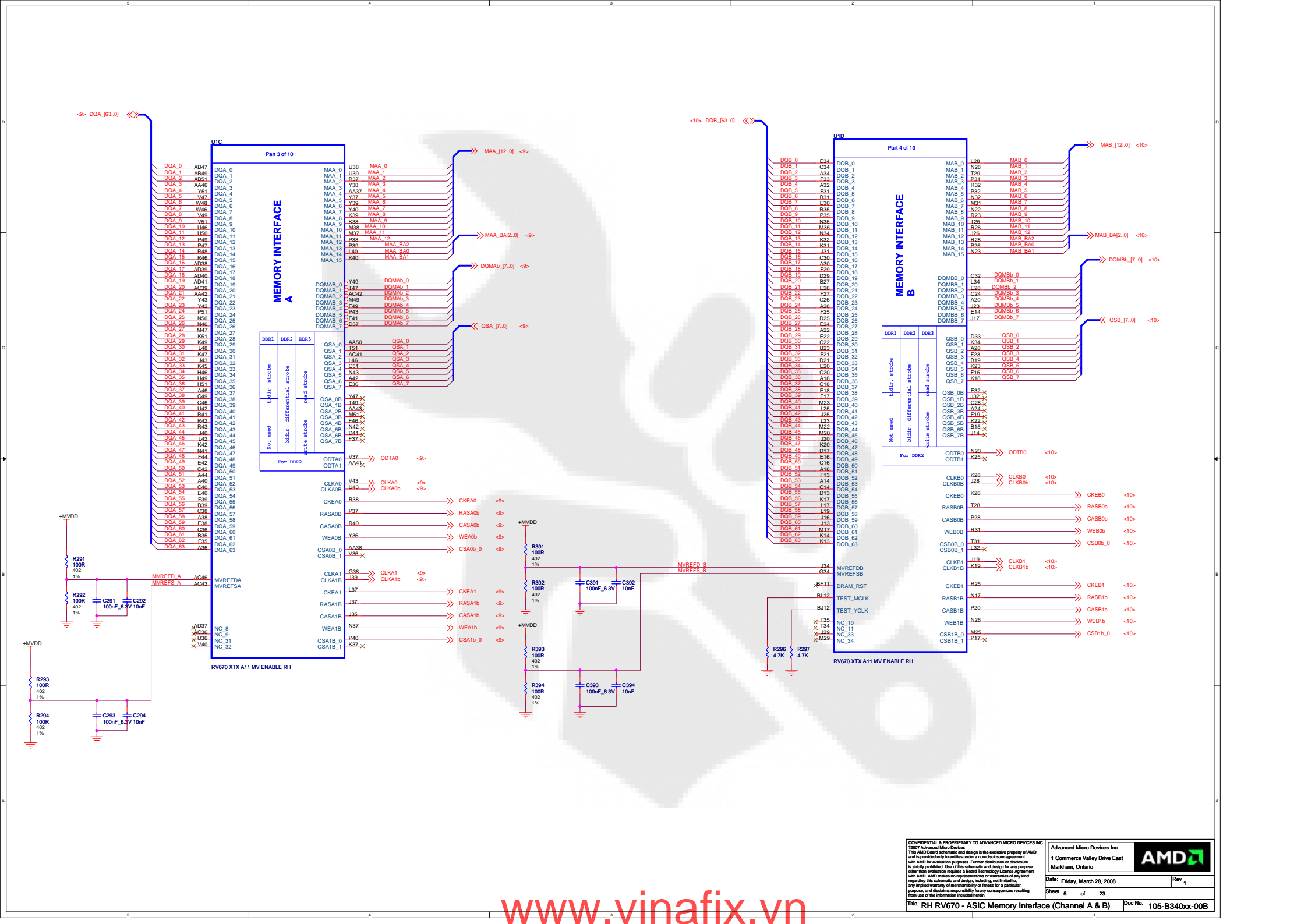
PCI-EXPRESS EDGE CONNECTOR

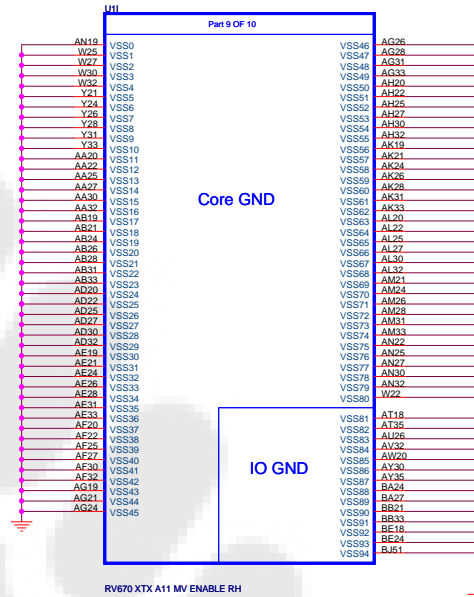
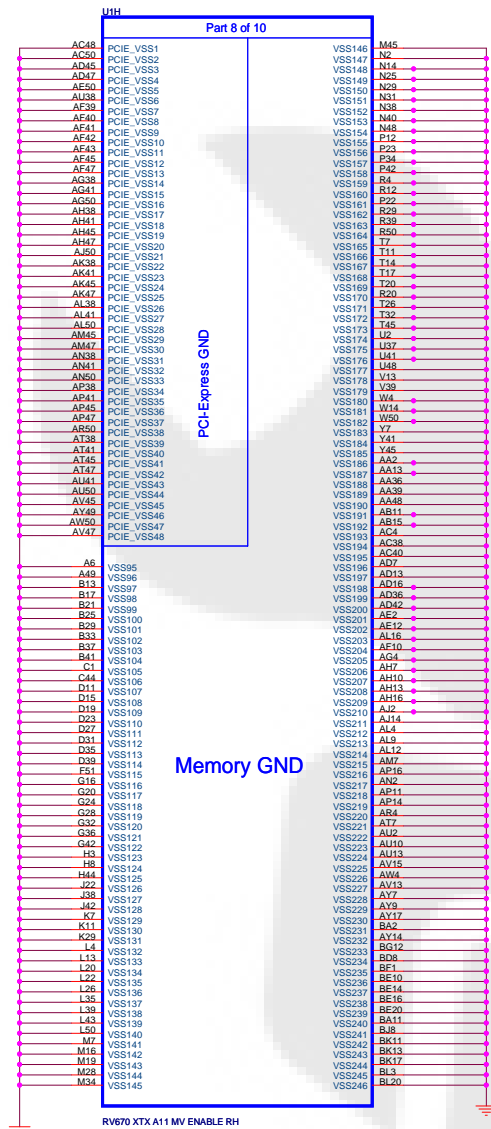


SYMBOL LEGEND	
DN1	DO NOT INSTALL
#	ACTIVE LOW
	DIGITAL GROUND
	ANALOG GROUND
BUO	BRING UP ONLY

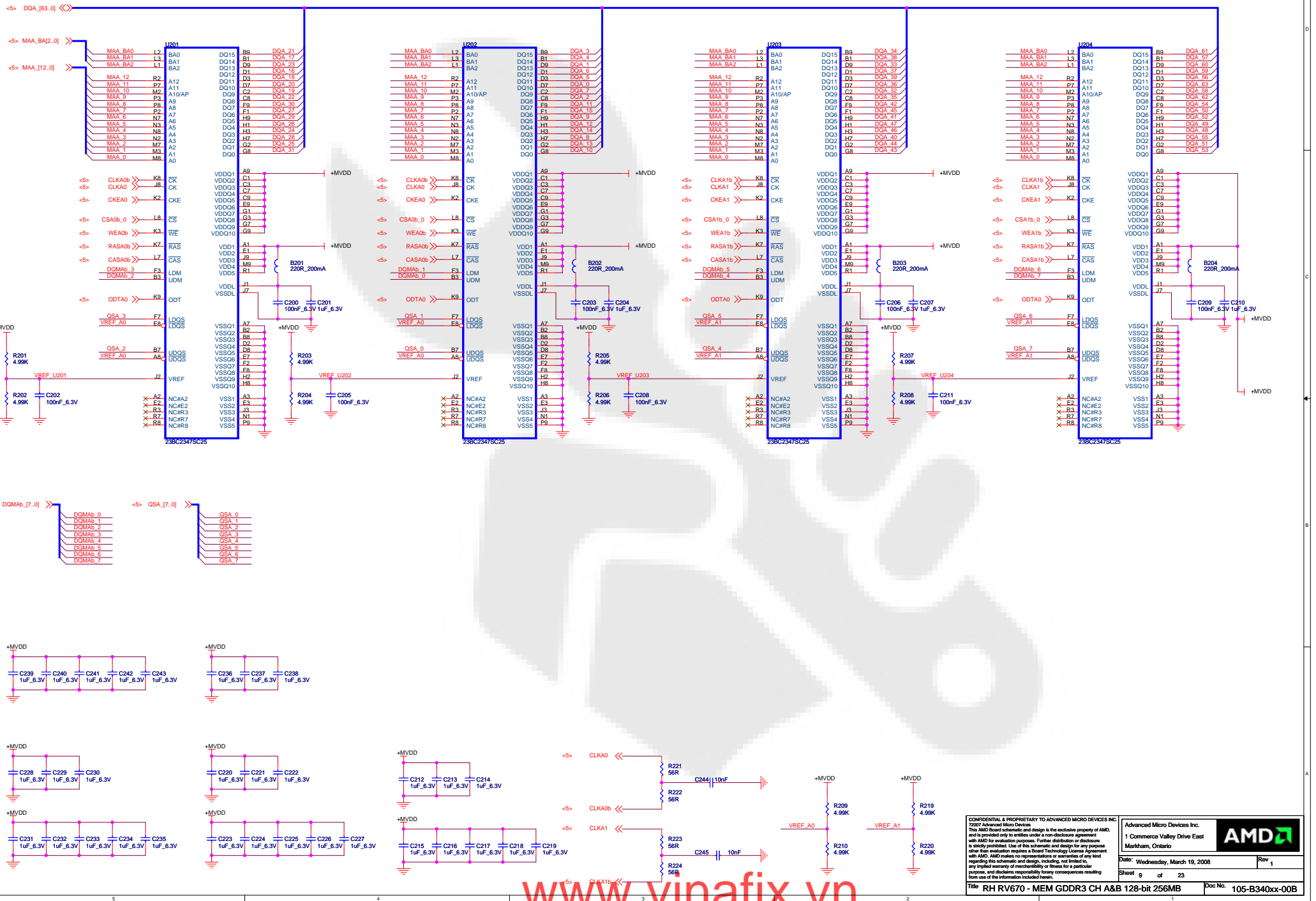
Recommended caps:
(see BOM for qualified values/vendors)
10uF , X6S, 0805, 6.3V, 1.4MM MAX THICK
4.7uF , X6S/X5R, 0603, 6.3V/4V
1uF, X6S, 0402, 6.3V
100nF, X7R, 0402
10nF , X7R, 0402



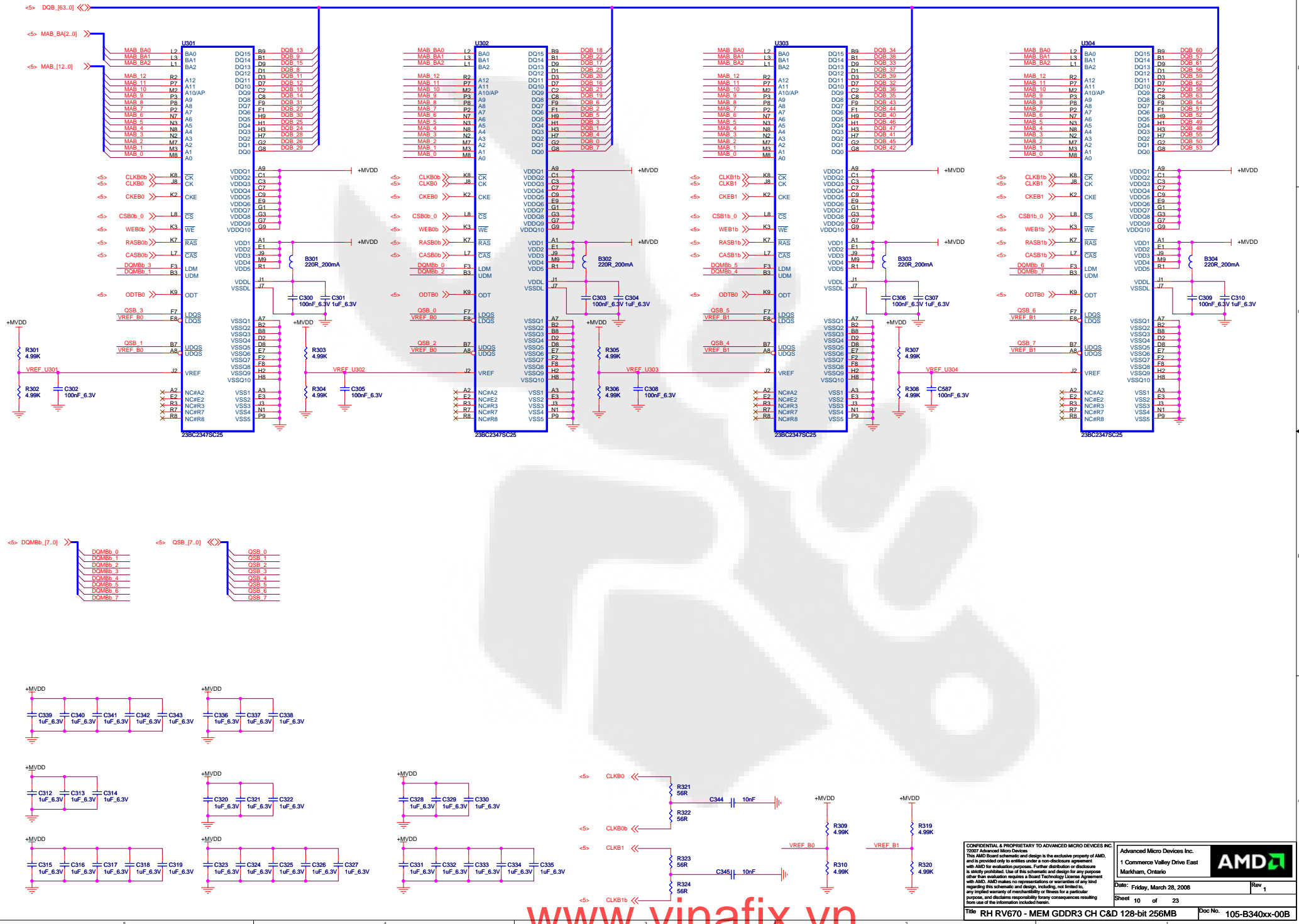




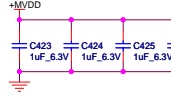
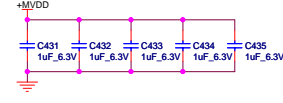
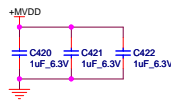
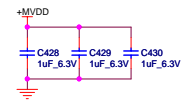
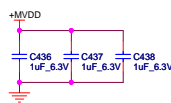
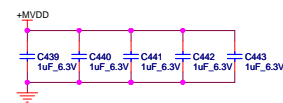
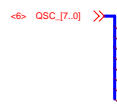
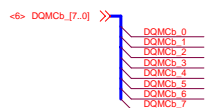
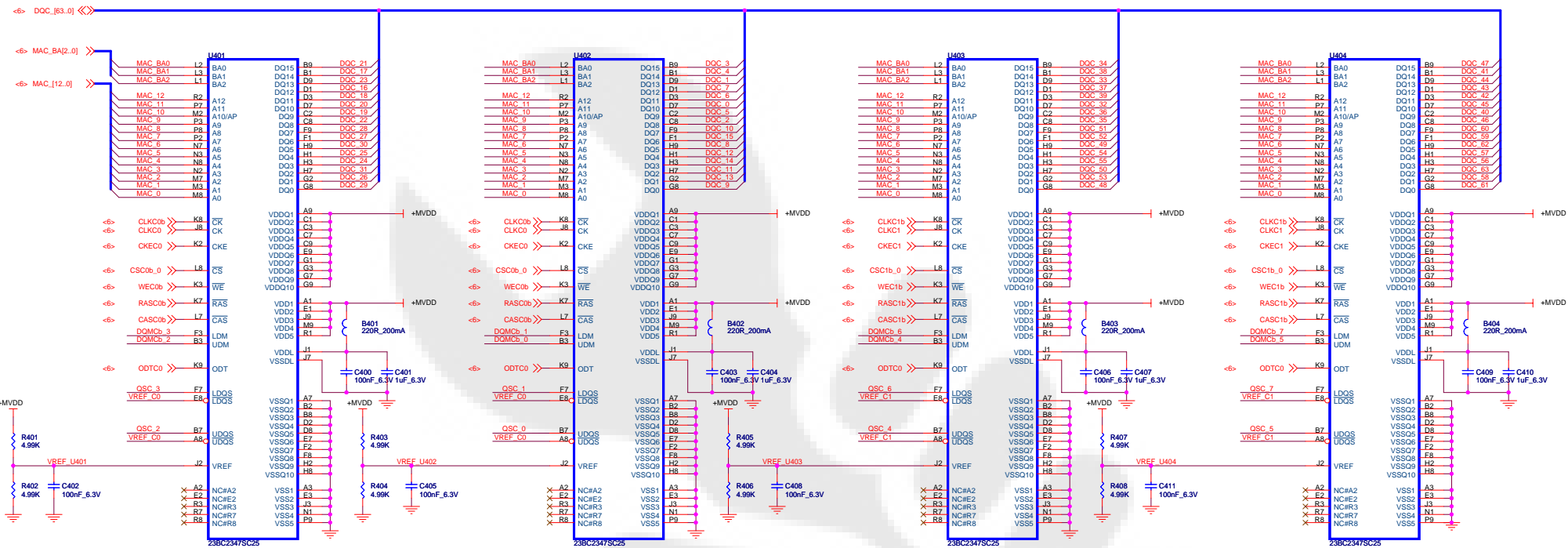
CHANNEL A: 128MB/256MB DDR2



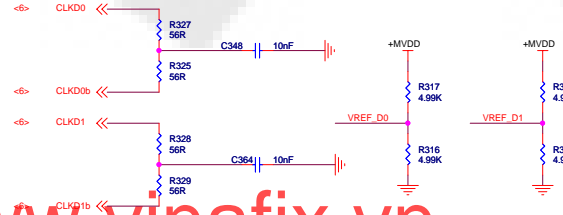
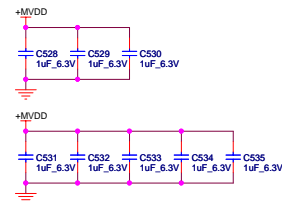
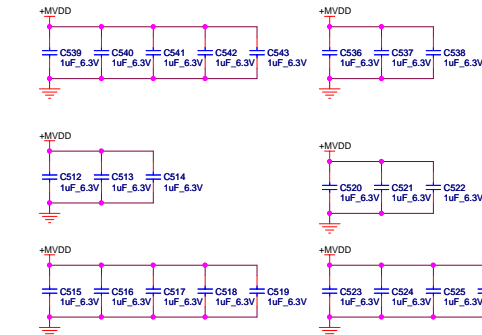
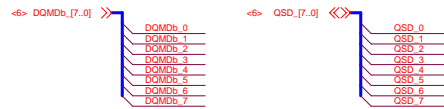
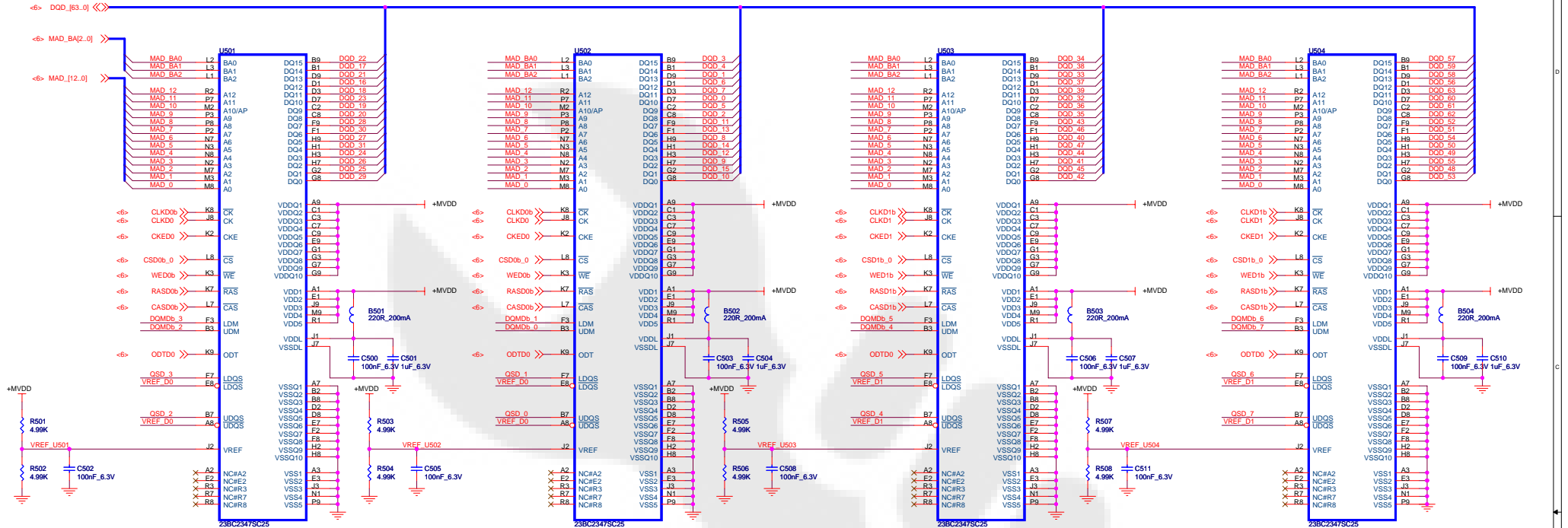
CHANNEL B: 128MB/256MB DDR2



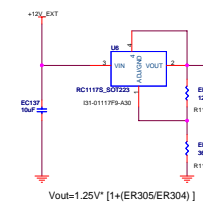
CHANNEL C: 128MB/256MB DDR2



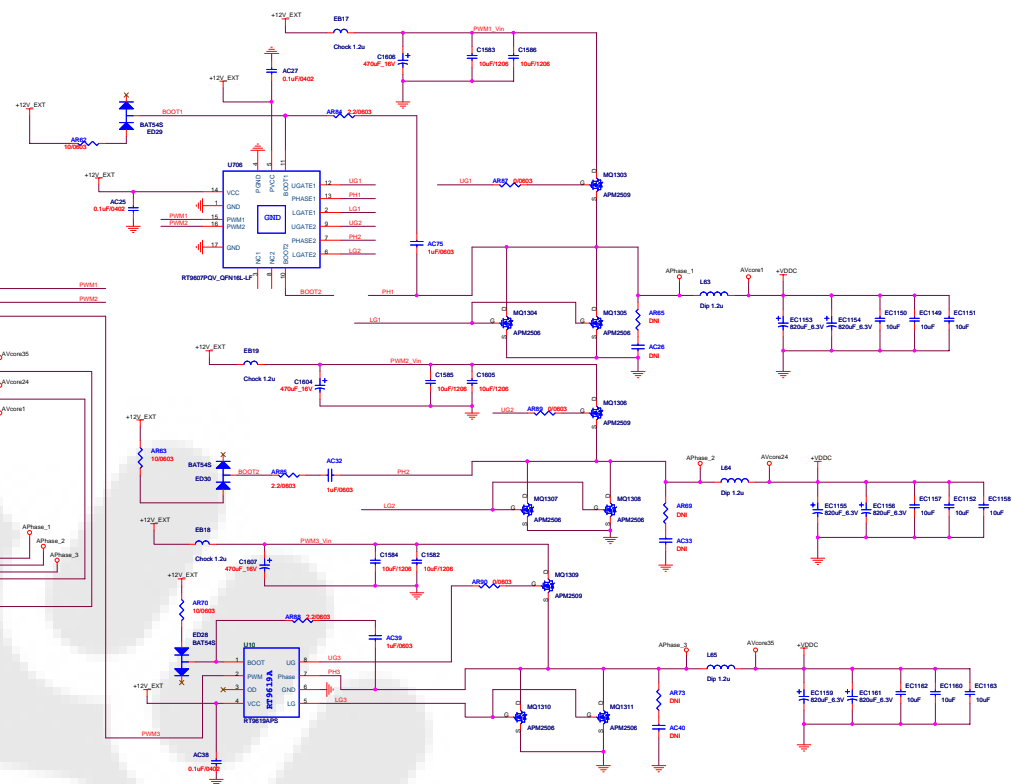
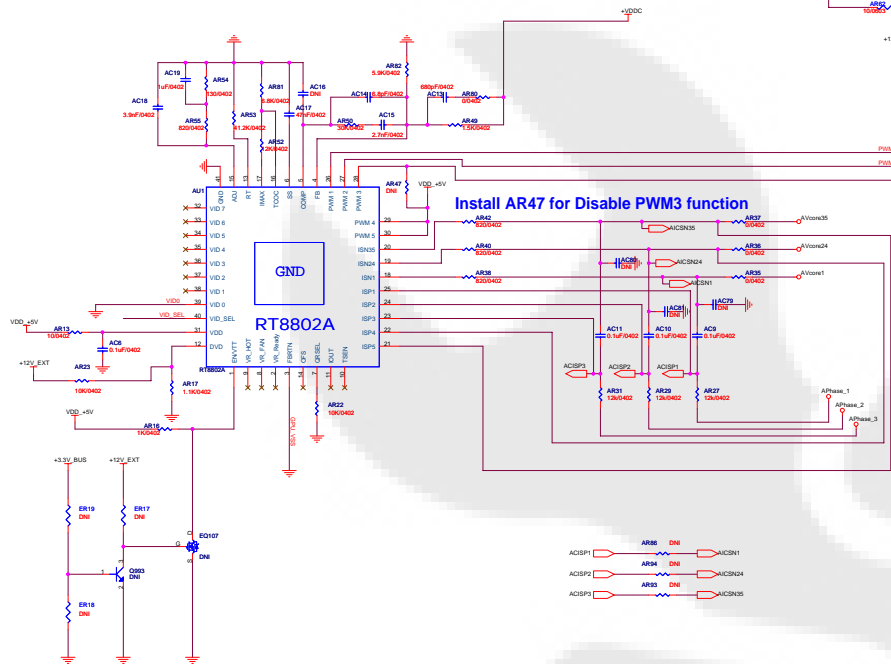
CHANNEL D: 128MB/256MB DDR2



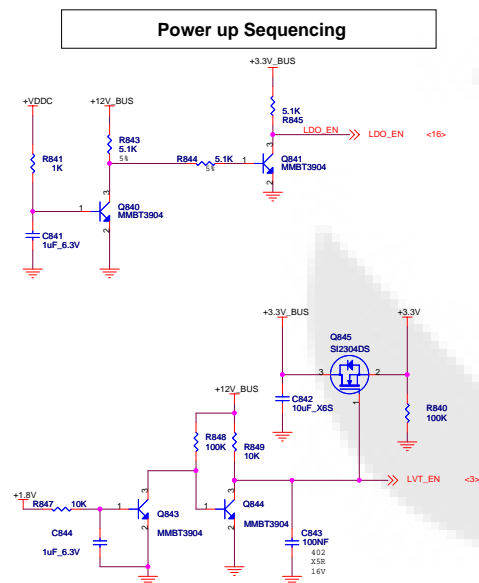
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Date	Friday, March 28, 2008	Sheet 12 of 23

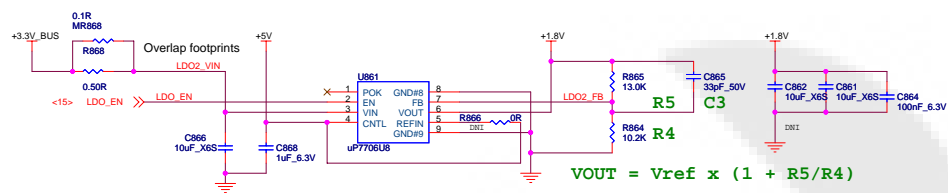


Install AR47 for Disable PWM3 function

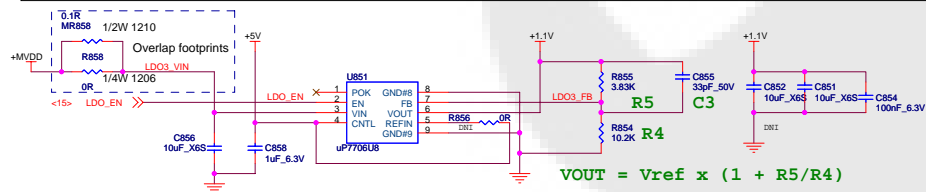


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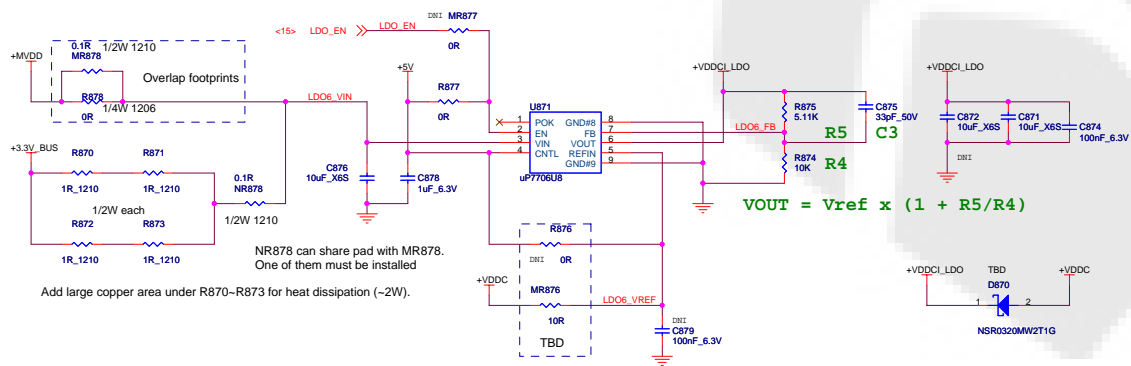


LDO #3: Vin = +1.70V to 2.1VMAX Vout = +1.1V +/- 3% Iout = Up to 1.3A (TBV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling

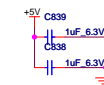
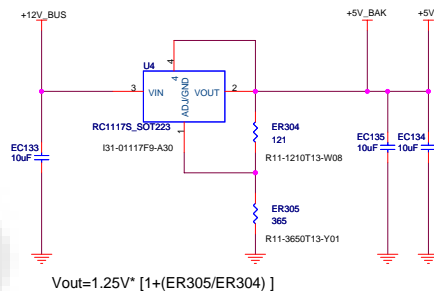





LDO #6: For fixed output voltage: $V_{in} = +1.70V$ to $2.1V$ MAX $V_{out} = +1.20V \pm 3\%$ $I_{out} = 1.3A$ (TBV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling

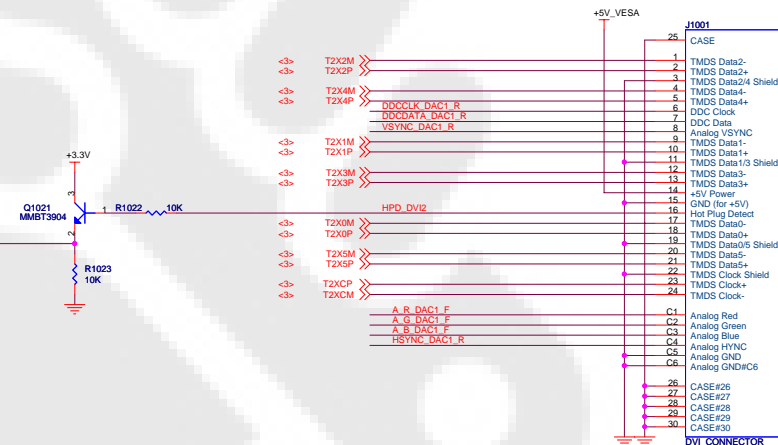
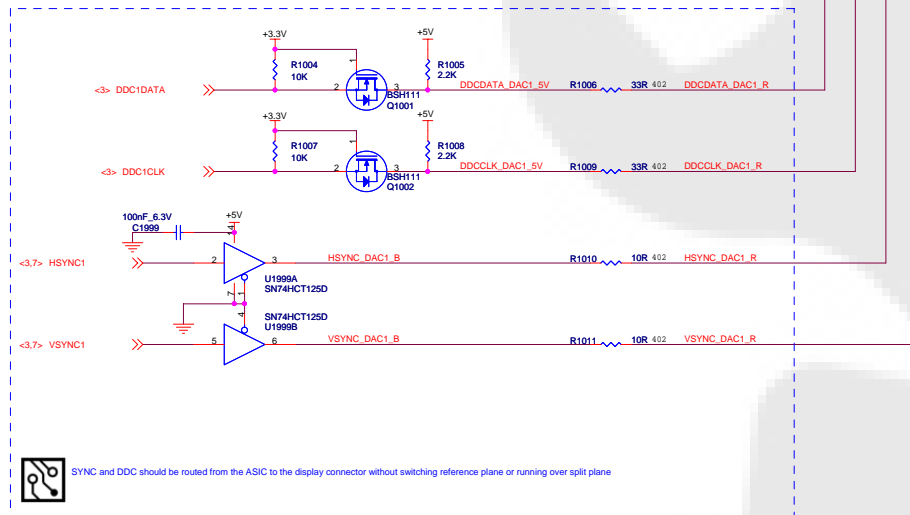
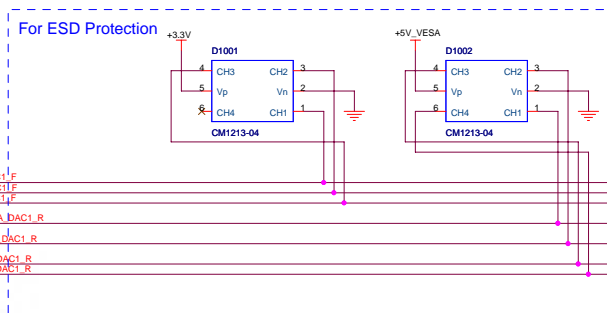
LDO #6:	For tracking VDDC:	Vin = TBD	Vout = TBD	Iout = 1.3A (TBV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling				



Regulators for +5V, +5V_VESA and +5V_VESA2

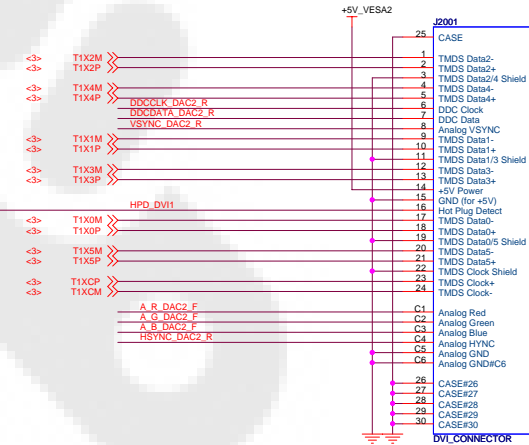
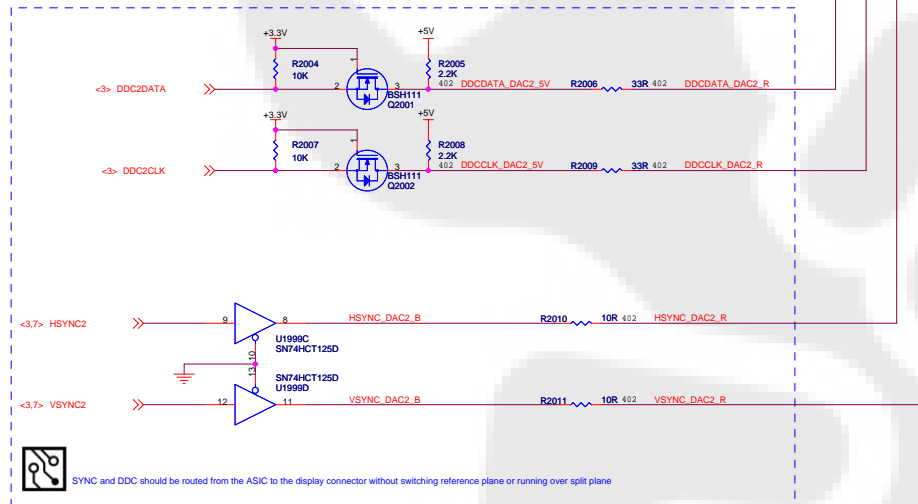
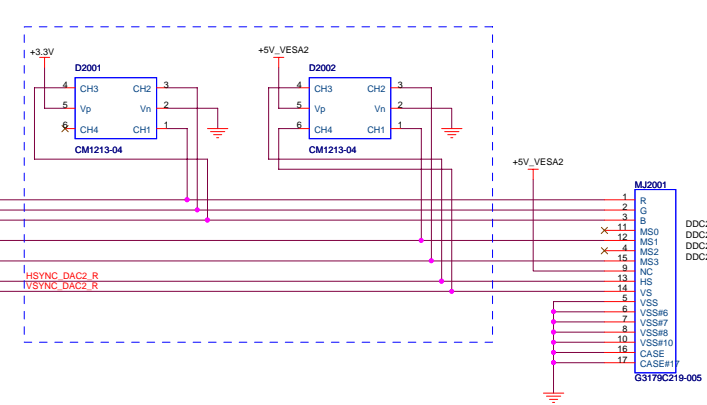
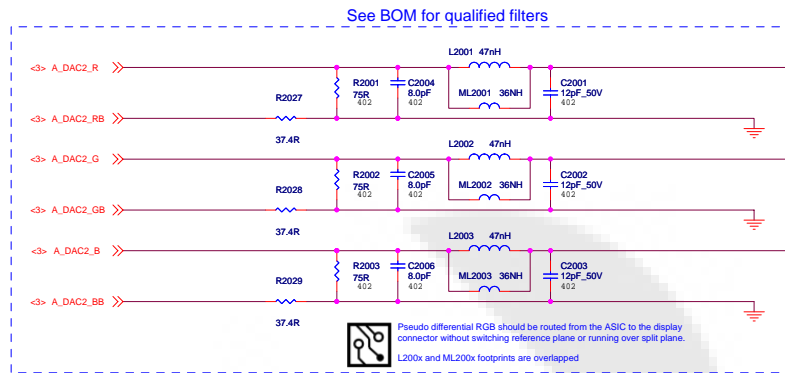


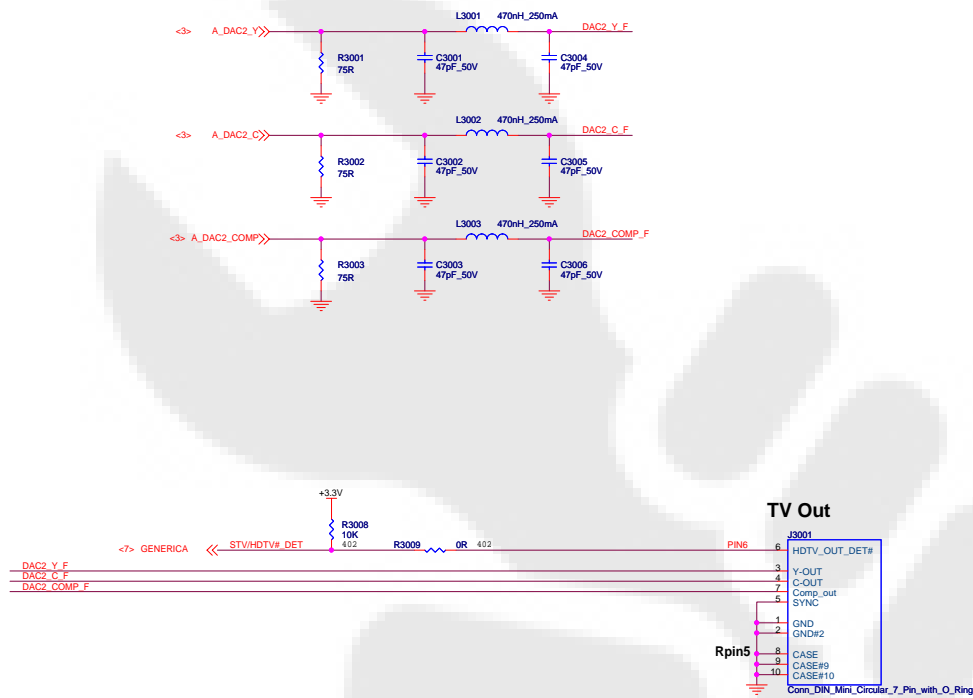
<p>CONFIDENTIAL & PROPRIETARY TO ADVANCED MICRO DEVICES INC. 7200 Advanced Micro Devices This AMD Board schematic and design is the exclusive property of AMD, and is provided only to entities under a non-disclosure agreement with AMD. This document and its contents are not to be distributed, copied, or otherwise publicly disclosed without the express written permission of AMD. This document is property prohibited. Use of this schematic and design for any purpose other than that for which it was created is strictly prohibited. Any use of this document for evaluation requires a Board Technology License Agreement with AMD. AMD makes no representations or warranties of any kind regarding the accuracy and design, including but not limited to, any implied warranty of merchantability or fitness for a particular purpose, and disclaims responsibility for any consequences resulting from use of the information included herein.</p>	<table border="1"><tr><td colspan="2" data-bbox="1789 1449 1991 1452">Advanced Micro Devices Inc. 1 Commerce Valley Drive East Markham, Ontario L3R 9V5</td></tr><tr><td colspan="2" data-bbox="1789 1452 1991 1468"></td></tr><tr><td data-bbox="1789 1461 1991 1465">Date: Wednesday, March 19, 2008</td><td data-bbox="1789 1465 1991 1466">Rev 1</td></tr><tr><td data-bbox="1789 1466 1991 1468">Sheet 16 of 23</td><td data-bbox="1789 1468 1991 1469">Juc No: 105-B340xx-00B</td></tr></table>	Advanced Micro Devices Inc. 1 Commerce Valley Drive East Markham, Ontario L3R 9V5				Date: Wednesday, March 19, 2008	Rev 1	Sheet 16 of 23	Juc No: 105-B340xx-00B
Advanced Micro Devices Inc. 1 Commerce Valley Drive East Markham, Ontario L3R 9V5									
									
Date: Wednesday, March 19, 2008	Rev 1								
Sheet 16 of 23	Juc No: 105-B340xx-00B								



D815 pin	Standard VGA	DDC1 Host	DDC2B or DDC2B+ Host	DDC2AB Host	DDC1/2 Dis
11	Monitor ID bit 0	Monitor ID bit 0	Monitor ID bit 0	Monitor ID bit 0	Optional
12	Data from disp	Data from disp	Data from disp	Data from disp	Optional
4	Monitor ID bit 2	Monitor ID bit 2	Monitor ID bit 2	Monitor ID bit 2	Optional
15	Monitor ID bit 3	Open	SCL	SCL	SCL
9	N/C	+5V 50mA min 1A max	+5V 50mA min 1A max	+5V 300mA min 1A max	Optional
Hardware Support	No	Yes	Yes	No	Yes

Based on VESA Display Data Channel (DDC) Standard Ver. 3 Dec. 15, 1997





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 Markham, Ontario



Date: Wednesday, March 10, 2008

Rev 1

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Title RH RV670 - TV OUT

Doc No. 105-B340xx-00B

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ASSY-SCREW1
SCREW
JACKPOST, HEX, 3/16 AF, 4-40 INT/EXT
<3rd part field>

ASSY-SCREW2
SCREW
JACKPOST, HEX, 3/16 AF, 4-40 INT/EXT
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ASSY-SCREW3
SCREW
JACKPOST, HEX, 3/16 AF, 4-40 INT/EXT
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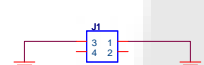
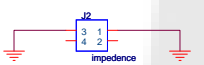
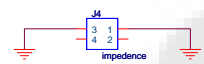
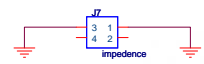
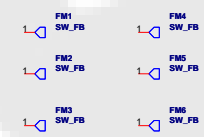
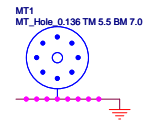
ASSY1
ANTISTATIC
BAG
6_X_11

BKT1
BRACKET
8020038600G

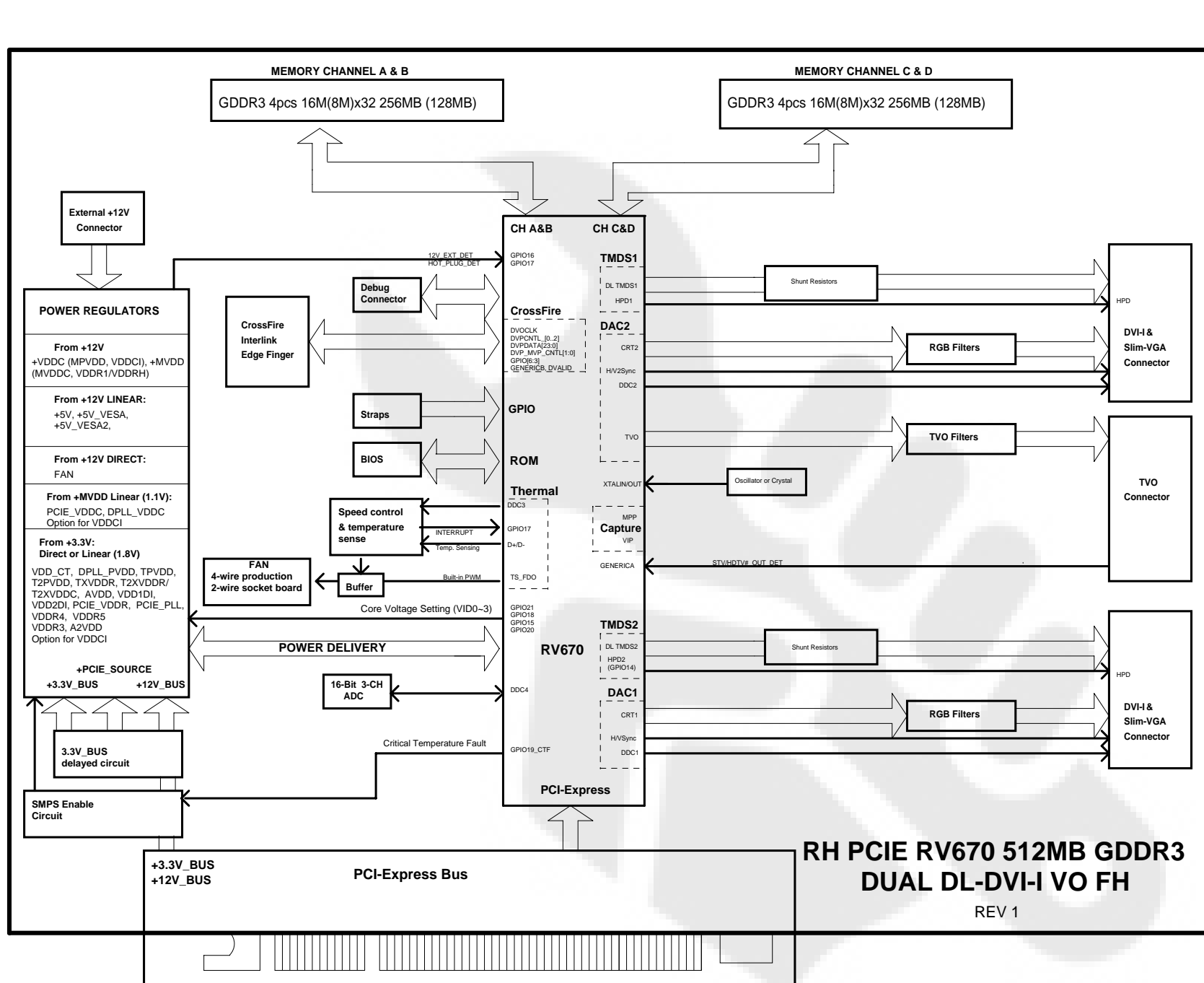
BKT2
BRACKET
80200386B0G

ASSY-SCREW5
SCREW
SCREW

BKT1: DVI - DIN-DVI
BKT2: DVI - DVI
ASSY-SCREWS only for Slim-VGA with upper or lower tab bracket







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Markham, Ontario



Date: Thursday, March 13, 2008

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Title RH PCIE RV670 512MB GDDR3 DUAL DL-DVI-I VO FH Doc No. 105-B340xx-00B

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